

### • General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ . It combines one N channel MOSFET and one P channel MOSFET.

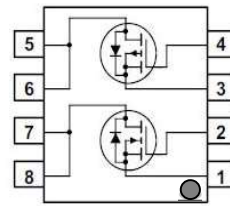
### • Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Dual DIE in one package

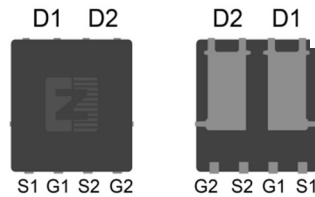
### • Application

- Power Management in Notebook Computer
- BLDC Motor driver

### • Product Summary



$V_{DS1} = 40V$   
 $V_{DS2} = -40V$   
 $R_{DS(ON)1} = 13m\Omega$   
 $R_{DS(ON)2} = 29m\Omega$   
 $I_{D1} = 26A$   
 $I_{D2} = -18A$



DFN5\*6

### • Ordering Information:

Part NO.	ZMC88403N
Marking	ZMC88403
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

### • Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}$	-	-	1.5	$^{\circ}C/W$
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	37	$^{\circ}C/W$
Soldering temperature, wavesoldering for 10s	$T_{sold}$	-	-	265	$^{\circ}C$

### • N Channel Absolute Maximum Ratings ( $T_c = 25^{\circ}C$ )

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_{D@TC=25^{\circ}C}$	26	A
	$I_{D@TC=75^{\circ}C}$	20	A
	$I_{D@TC=100^{\circ}C}$	16	A

Pulsed Drain Current <sup>①</sup>	$I_{DM}$	78	A
Total Power Dissipation	$P_D@T_C=25^\circ\text{C}$	42	W
Total Power Dissipation	$P_D@T_A=25^\circ\text{C}$	1.7	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$
Single Pulse Avalanche Energy	$E_{AS}$	55	mJ

#### •P Channel Absolute Maximum Ratings ( $T_C = 25^\circ\text{C}$ )

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D@T_C=25^\circ\text{C}$	-18	A
	$I_D@T_C=75^\circ\text{C}$	-13.6	A
	$I_D@T_C=100^\circ\text{C}$	-11	A
Pulsed Drain Current <sup>①</sup>	$I_{DM}$	-54	A
Total Power Dissipation	$P_D@T_C=25^\circ\text{C}$	42	W
Total Power Dissipation	$P_D@T_A=25^\circ\text{C}$	1.7	W
Operating Junction Temperature	$T_J$	-55 to 150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$
Single Pulse Avalanche Energy	$E_{AS}$	51	mJ

#### •N Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu\text{A}$	40			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	1.2		2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS} = 40V, V_{GS} = 0V$			1.0	$\mu\text{A}$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 8A$		13	17	$\text{m}\Omega$
		$V_{GS} = 4.5V, I_D = 6A$		18	24	$\text{m}\Omega$
Forward Trans conductance	$g_{FS}$	$V_{DS} = 25V, I_D = 6A$		10		s
Source-drain voltage	$V_{SD}$	$I_S = 8A$			1.28	V

**•Dynamic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	Ciss	f = 1MHz V <sub>DS</sub> =25V	-	1150	-	pF
Output capacitance	Coss		-	290	-	
Reverse transfer capacitance	Crss		-	205	-	
Total gate charge	Qg	V <sub>DD</sub> =25V	-	16	-	nC
Gate - Source charge	Qgs	I <sub>D</sub> = 6A	-	6	-	
Gate - Drain charge	Qgd	V <sub>GS</sub> = 10V	-	8	-	

**•P Channel Electronic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-40			V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1.2		-2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V			1.0	uA
Gate- Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V			±100	nA
Static Drain-source On Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A		29	37	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6A		46	60	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-6A		6		s

**•Dynamic Characteristics**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	Ciss	f = 1MHz V <sub>DS</sub> =-25V	-	1250	-	pF
Output capacitance	Coss		-	300	-	
Reverse transfer capacitance	Crss		-	215	-	
Total gate charge	Qg	V <sub>DD</sub> = -25V	-	16	-	nC
Gate - Source charge	Qgs	I <sub>D</sub> = -8A	-	6	-	
Gate - Drain charge	Qgd	V <sub>GS</sub> = -10V	-	8	-	

•N Channel characteristics curve

Fig.1 Power Dissipation Derating Curve

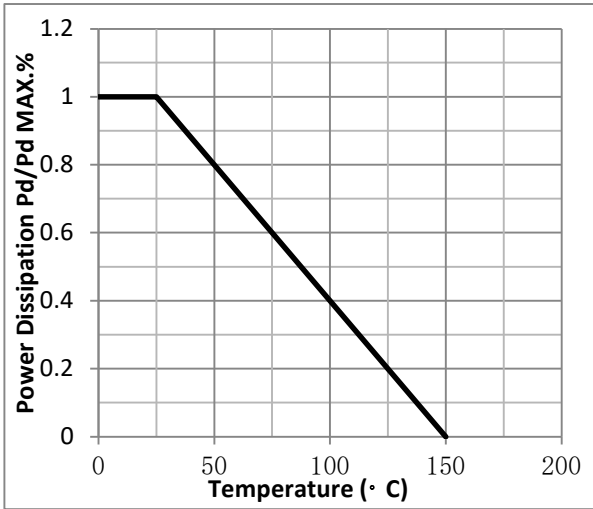


Fig.2 Typical output Characteristics

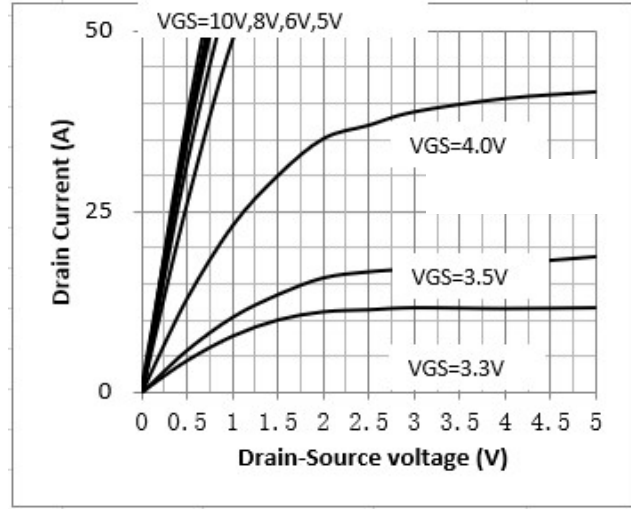


Fig.3 Threshold Voltage V.S Junction Temperature

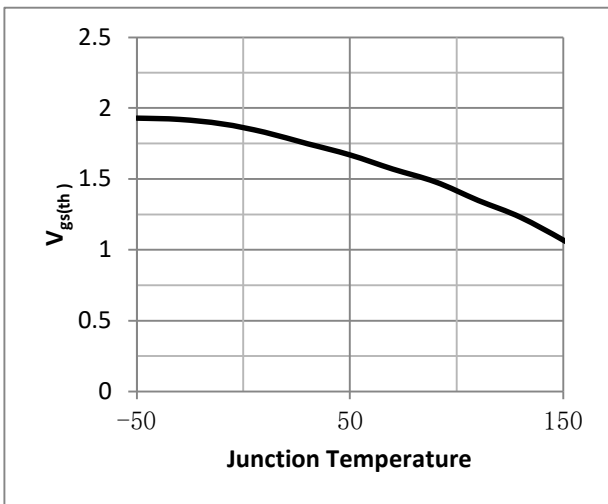


Fig.4 Resistance V.S Drain Current

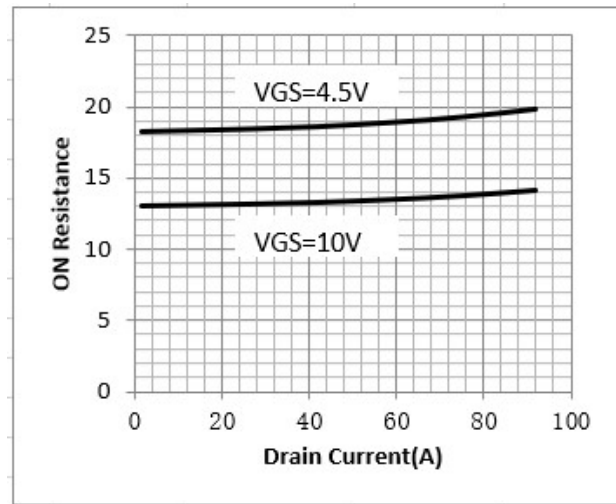


Fig.5 On-Resistance VS Gate Source Voltage

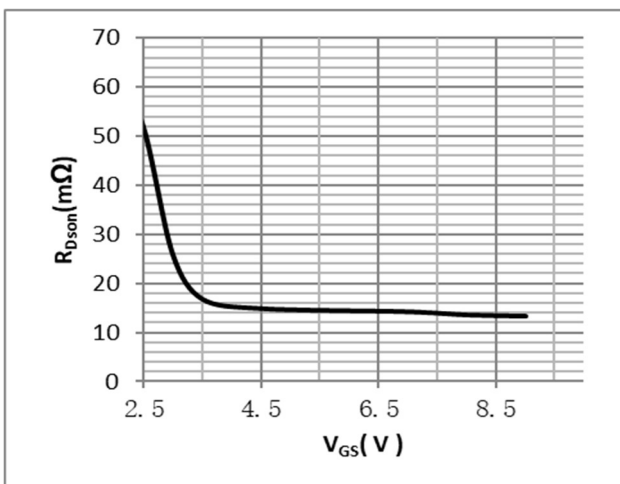
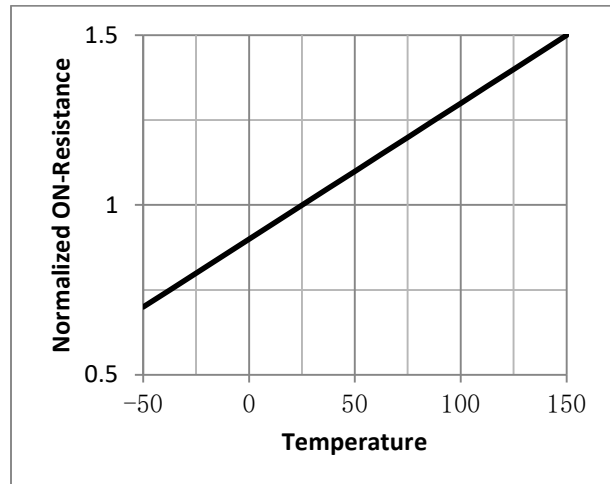


Fig.6 On-Resistance V.S Junction Temperature



•P Channel characteristics curve

Fig.1 Power Dissipation Derating Curve

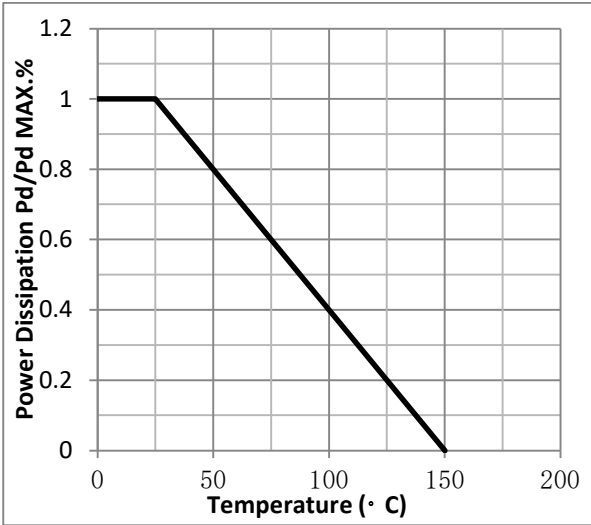


Fig.2 Typical output Characteristics

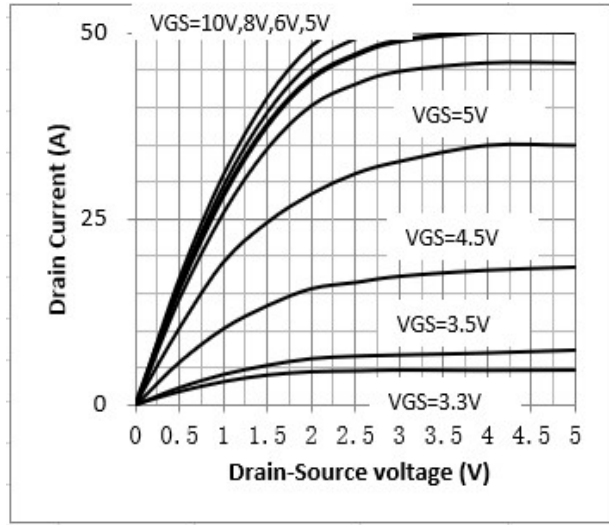


Fig.3 Threshold Voltage V.S Junction Temperature

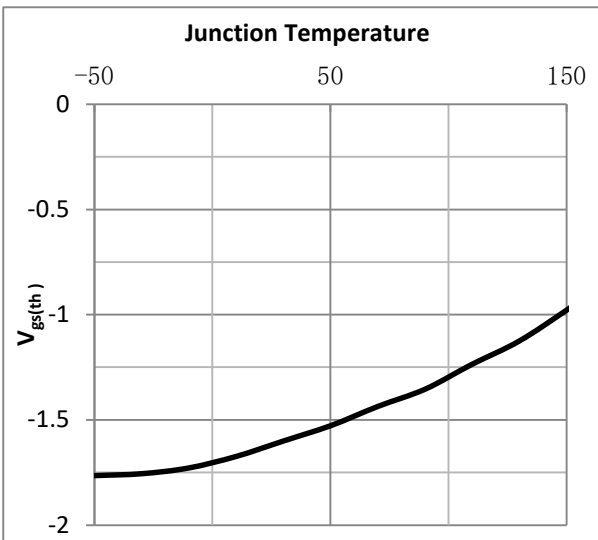


Fig.4 Resistance V.S Drain Current

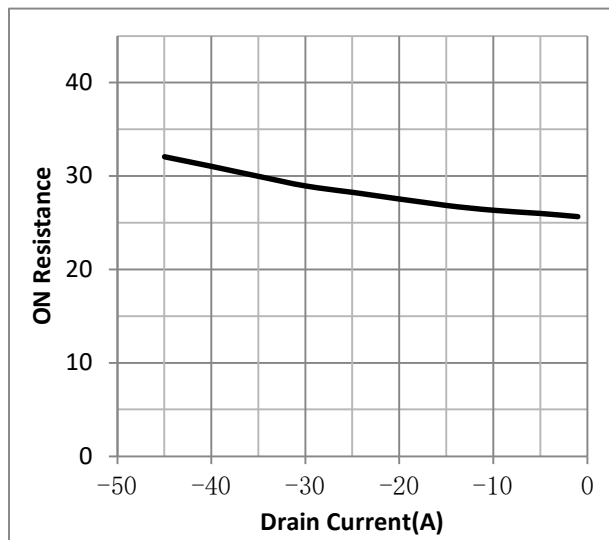


Fig.5 On-Resistance VS Gate Source Voltage

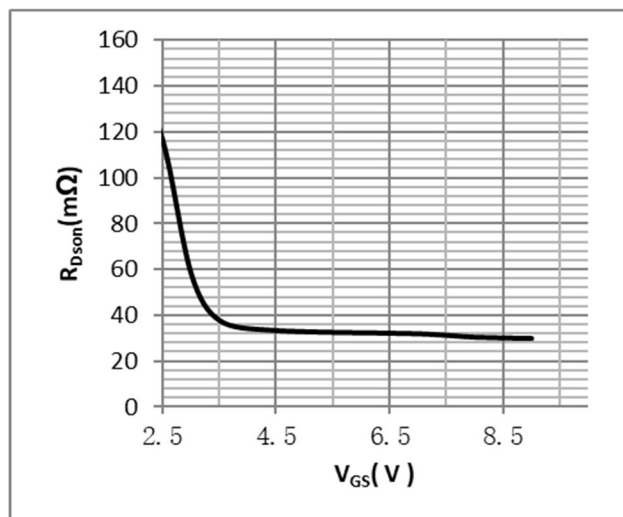
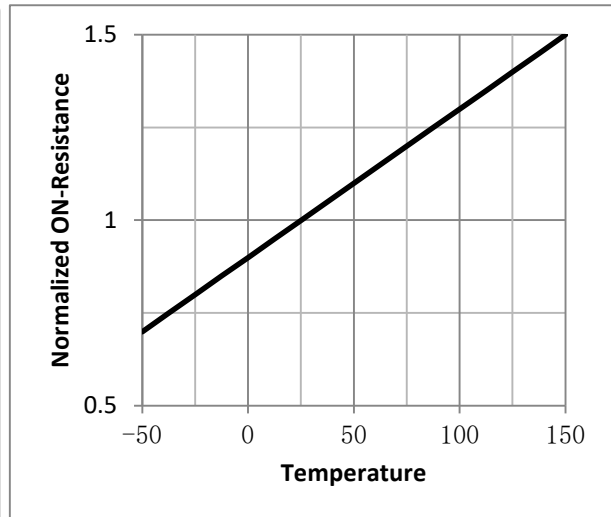


Fig.6 On-Resistance V.S Junction Temperature



•Test Circuit

Fig.1 Gate Charge Measurement Circuit

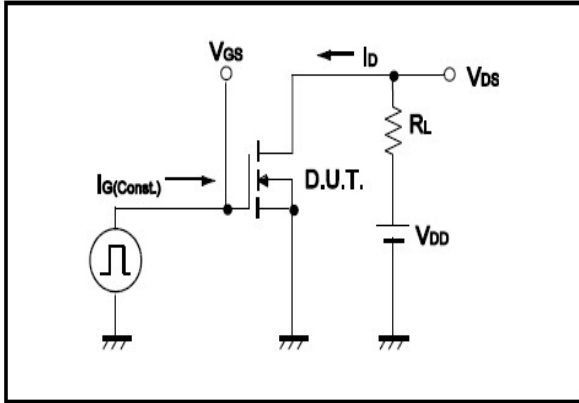


Fig.2 Gate Charge Waveform

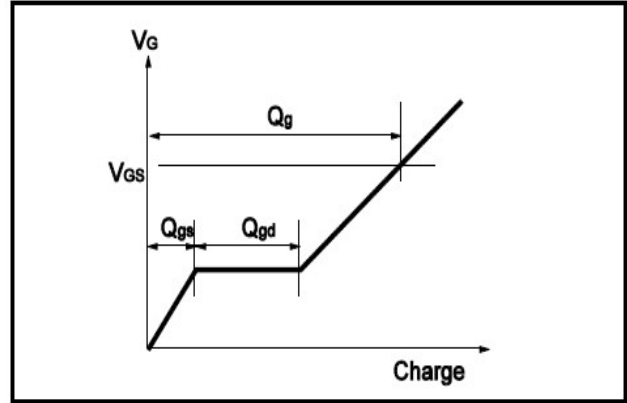


Fig.3 Switching Time Measurement Circuit

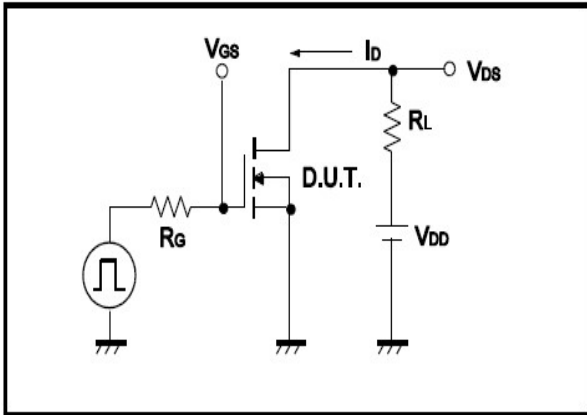


Fig.4 Switching Time Waveform

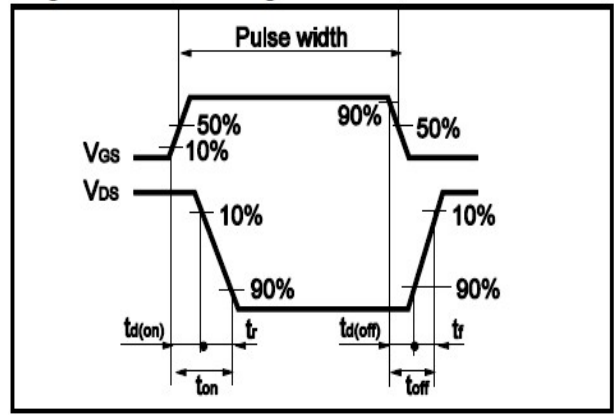


Fig.5 Avalanche Measurement Circuit

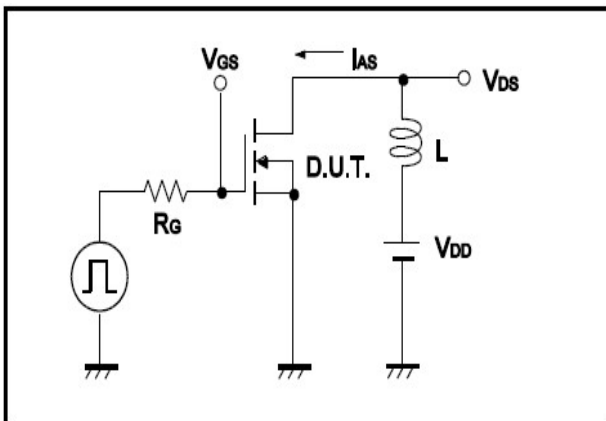
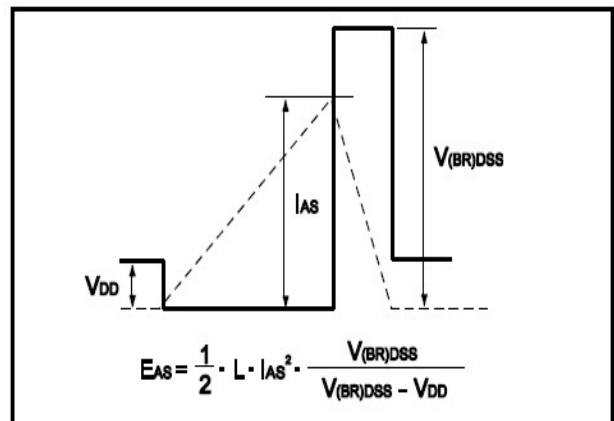
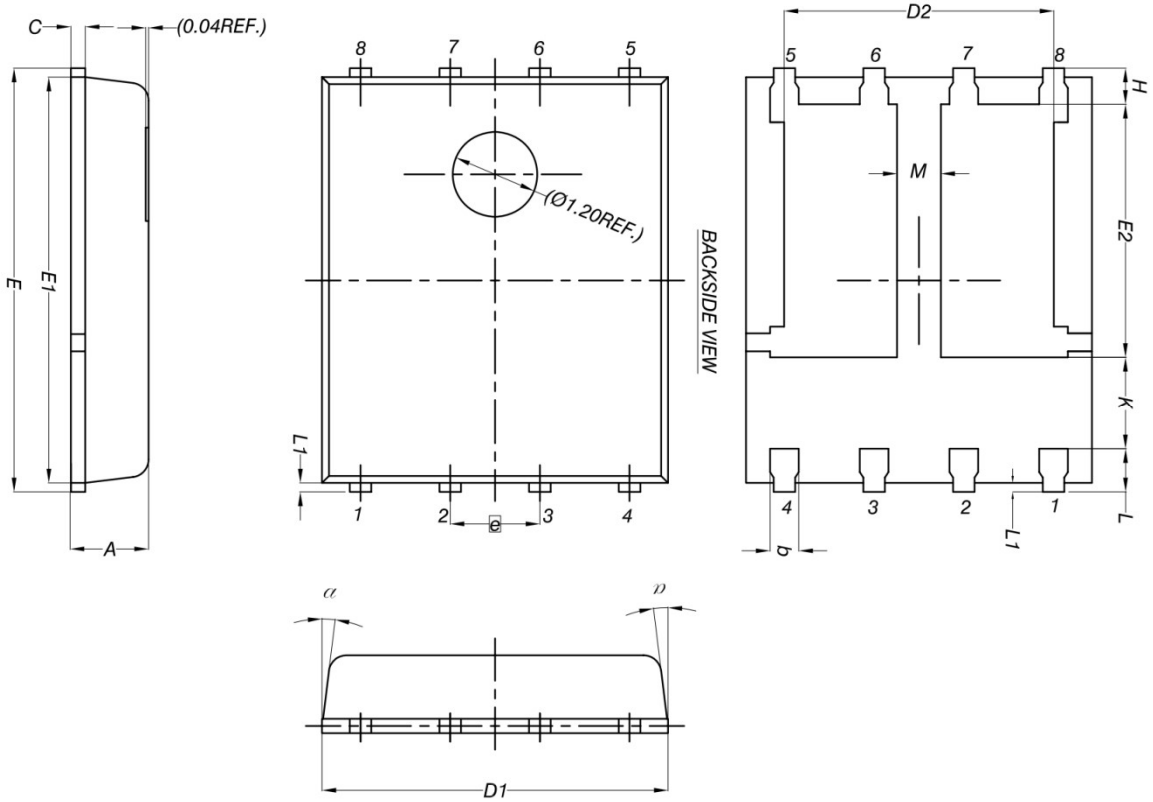


Fig.6 Avalanche Waveform





•Dimensions (DFN5×6)



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
<span style="border: 1px solid black; padding: 2px;">e</span>	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

